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1 **A prediction algorithm for feedback control models with long delays**
Jang, B.S.; Kim, B.G.; Pecelli, G.;
Broadband Switching Systems, 1997. Proceedings. 2nd IEEE International Workshop on , 2-4 Dec. 1997
Pages:131 - 136

[Abstract] [PDF Full-Text (320 KB)] **IEEE CNF**

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☐	L15	L13 and gate input	0
☐	L14	L13 and transition voltage	0
☐	L13	L12 and resistance	28
☐	L12	L11 and driver	31
☐	L11	L10 and gate	40
☐	L10	L8 and cell	44
☐	L9	L8 and ramp response	0
☐	L8	L6 and capacitance	103
☐	L7	L6 and slope delay	3
☐	L6	L5 and switching threshold	199
☐	L5	switch$ and threshold and slope and delay	8239
☐	L4	L3 and slope delay	0
☐	L3	switching threshold delay	8
☐	L2	(5379231)[CCLS]	0
☐	L1	(5379231)![CCLS]	0

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☐ 1. Document ID: US 6535987 B1

Using default format because multiple data bases are involved.

L7: Entry 1 of 3 File: USPT Mar 18, 2003

US-PAT-NO: 6535987
DOCUMENT-IDENTIFIER: US 6535987 B1

TITLE: Amplifier with a fan-out variable in time

DATE-ISSUED: March 18, 2003

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Ferrant; Richard	Saint Ismier			FR

US-CL-CURRENT: 713/401; 327/170, 716/5

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw De

☐ 2. Document ID: US 6473725 B1

L7: Entry 2 of 3 File: USPT Oct 29, 2002

US-PAT-NO: 6473725
DOCUMENT-IDENTIFIER: US 6473725 B1
**** See image for Certificate of Correction ****

TITLE: Method of accurate simulation of logic circuits

DATE-ISSUED: October 29, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Schoellkopf; Jean-Pierre	Grenoble			FR
Hanriat; Stephane	Saint Vincent de Mercuze			FR

US-CL-CURRENT: 703/15; 703/18, 716/2, 716/6

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw De

☐ 3. Document ID: US 5378946 A

L7: Entry 3 of 3 File: USPT Jan 3, 1995

US-PAT-NO: 5378946
DOCUMENT-IDENTIFIER: US 5378946 A
**** See image for Certificate of Correction ****

TITLE: Arrangement for temporal detection of a signal edge of an electrical signal transmitted over a transmission line

DATE-ISSUED: January 3, 1995

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Reime; Gerd	Schomberg			DE

US-CL-CURRENT: 327/14; 327/263, 327/361, 327/78

Term	Documents
SLOPE	157595
SLOPES	53254
DELAY	367414
DELAYS	123232
(6 AND (SLOPE ADJ DELAY)).PGPB,USPT.	3
(L6 AND SLOPE DELAY).PGPB,USPT.	3

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Search Results - Record(s) 1 through 8 of 8 returned.

☐ 1. Document ID: US 6359478 B1

Using default format because multiple data bases are involved.

L3: Entry 1 of 8 File: USPT Mar 19, 2002

US-PAT-NO: 6359478
DOCUMENT-IDENTIFIER: US 6359478 B1

TITLE: Reduced-undershoot CMOS output buffer with delayed VOL-driver transistor

DATE-ISSUED: March 19, 2002

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Chow; Arnold	Sunnyvale	CA		

US-CL-CURRENT: 327/112; 327/391

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 2. Document ID: US 6111443 A

L3: Entry 2 of 8 File: USPT Aug 29, 2000

US-PAT-NO: 6111443
DOCUMENT-IDENTIFIER: US 6111443 A

TITLE: Accelerated switching by selection of various threshold levels

DATE-ISSUED: August 29, 2000

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Mores; Robert	Hamburg			DE
Eisele; Harald	Pinneberg			DE

US-CL-CURRENT: 327/206; 327/166, 327/374, 327/74

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw D

☐ 3. Document ID: US 5859552 A

L3: Entry 3 of 8 File: USPT Jan 12, 1999

US-PAT-NO: 5859552
DOCUMENT-IDENTIFIER: US 5859552 A

TITLE: Programmable slew rate control circuit for output buffer

DATE-ISSUED: January 12, 1999

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Do; Tuan P.	San Jose	CA		
Stascausky; Casimiro A.	Fremont	CA		

US-CL-CURRENT: 327/170; 327/276, 327/281

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 4. Document ID: US 5189589 A

L3: Entry 4 of 8 File: USPT Feb 23, 1993

US-PAT-NO: 5189589
DOCUMENT-IDENTIFIER: US 5189589 A

TITLE: Power controller

DATE-ISSUED: February 23, 1993

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Marschall; Klaus W.	Meersburg			DE

US-CL-CURRENT: 361/93.2; 327/579, 361/111, 361/98

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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☐ 5. Document ID: US 5097148 A

L3: Entry 5 of 8 File: USPT Mar 17, 1992

US-PAT-NO: 5097148
DOCUMENT-IDENTIFIER: US 5097148 A

TITLE: Integrated circuit buffer with improved drive capability

DATE-ISSUED: March 17, 1992

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Gabara; Thaddeus J.	North Whitehall Township, Lehigh County	PA		

US-CL-CURRENT: 326/86; 326/27, 326/30, 326/71, 326/81

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw D

☐ 6. Document ID: US 4276487 A

L3: Entry 6 of 8 File: USPT Jun 30, 1981

US-PAT-NO: 4276487
DOCUMENT-IDENTIFIER: US 4276487 A

TITLE: FET driver circuit with short switching times

DATE-ISSUED: June 30, 1981

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Arzubi; Luis M.	Boeblingen			DE
Clemen; Rainer	Boeblingen			DE
Gschwendtner; Jorg	Esslingen			DE

US-CL-CURRENT: 326/88; 326/17

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw D

☐ 7. Document ID: US 4048664 A

L3: Entry 7 of 8 File: USPT Sep 13, 1977

US-PAT-NO: 4048664
DOCUMENT-IDENTIFIER: US 4048664 A

TITLE: Protective circuit for dynamic braking resistors

DATE-ISSUED: September 13, 1977

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Kirilloff; Victor V.	Murrysville	PA		

US-CL-CURRENT: 361/89; 338/53, 361/103

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWMC | Draw D

☐ 8. Document ID: US 3631464 A

L3: Entry 8 of 8 File: USPT Dec 28, 1971

US-PAT-NO: 3631464
DOCUMENT-IDENTIFIER: US 3631464 A

TITLE: DIGITAL PARALLEL TO SERIAL CONVERTER

DATE-ISSUED: December 28, 1971

INVENTOR-INFORMATION:

NAME	CITY	STATE	ZIP CODE	COUNTRY
Dahlberg; Roger C.	Palo Alto	CA		

US-CL-CURRENT: 341/101

Full | Title | Citation | Front | Review | Classification | Date | Reference | Claims | KWIC | Draw De

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Term	Documents
SWITCHING	458844
SWITCHINGS	1407
THRESHOLD	322439
THRESHOLDS	43526
DELAY	367414
DELAYS	123232
((SWITCHING ADJ THRESHOLD) ADJ DELAY).PGPB,USPT.	8
(SWITCHING THRESHOLD DELAY).PGPB,USPT.	8

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